



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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<i>PATENT & TRADEMARK OFFICE</i>				Application Number	10/088,205
				Filing Date	July 16, 2002
				First Named Inventor	Yasuhito TAKEO et al.
				Group Art Unit	2816
				Examiner Name	Linh M. Nguyen
Sheet	1	of	2	Attorney Docket Number	
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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code.
⁴For issuance patent documents, the date of issuance is the date of publication.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>MAR 19 2004 PATENT TRADEMARK OFFICE O I P E J C</i>				Complete if Known	
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Sheet	2	of	2	Examiner Name	Linh M. Nguyen
Attorney Docket Number 2469-111					
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			
<i>LmN</i>		ISHIHARA, N., "A Monolithic 156 Mb/s Clock and Data Recovery PLL Circuit Using the Sample-and-Hold Technique", IEEE J. Solid State Circuits, Vol. SC-29, pp. 1566-1571, Dec. 1994.			
<i>LmN</i>		HOGGE, JR., C., "A Self Correcting Clock Recovery Circuit", IEEE Journal of Lightwave Technology, Vol. LT-3, No. 6, pp. 1312-1314, Dec. 1985.			
Examiner Signature	<i>Linh M. Nguyen</i>			Date Considered	<i>10/06/2004</i>

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